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THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:

Daniel Xu et al.

Serial No.: 10/633,881

Filed: August 4, 2003

For: Forming Planarized
Semiconductor Structures

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Art Unit: 2812

Examiner: Alexander G. Ghyka

Atty Docket: ITO.0024US
P14742

Assignee: Intel Corporation

Mail Stop **Amendment**
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT

Sir:

In response to the restriction requirement mailed March 2, 2005, please amend the above-referenced patent application as follows:

Date of Deposit: March 11, 2005

I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Cynthia L. Hayden
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